

INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification⁶:

G06F

A2

(11) International Publication Number:

WO 98/20404

(43) International Publication Date:

14 May 1998 (14.05.98)

(21) International Application Number: PCT/US97/20026

(22) International Filing Date: 4 November 1997 (04.11.97)

(30) Priority Data:

08/743,515	4 November 1996 (04.11.96)	US
08/800,397	14 February 1997 (14.02.97)	US

(71)(72) Applicant and Inventor: CHARLES, Paul [US/US];
16290 A DeWitt Avenue, Morgan Hills, CA 95037 (US).

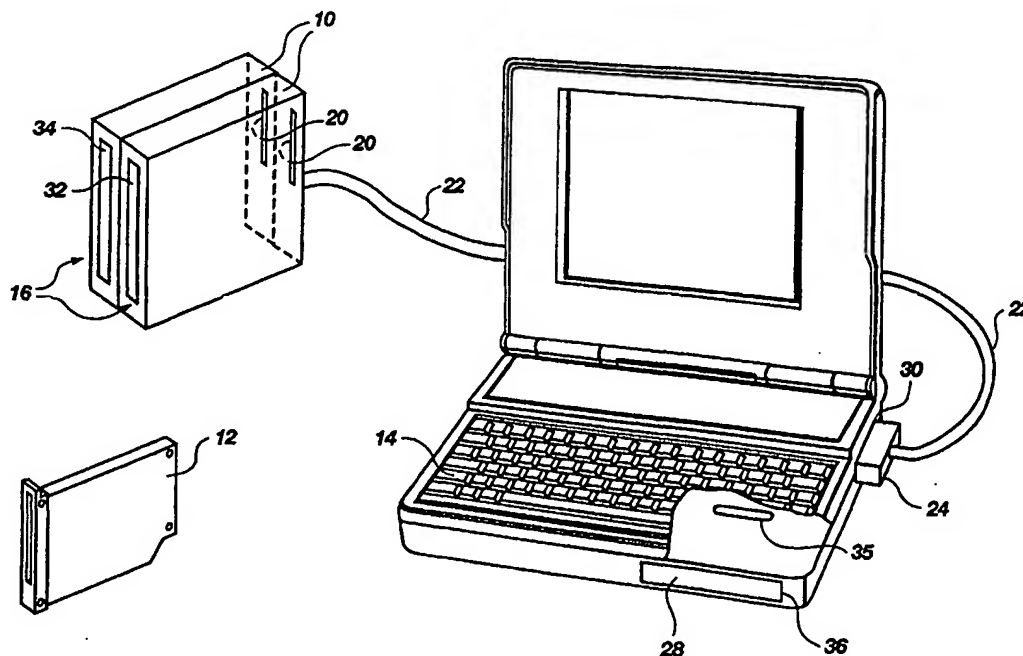
(72) Inventors; and

(75) Inventors/Applicants (for US only): MARSH, David [US/US];
6208 E. Juana Court, Cave Creek, AZ 85331 (US). LeV-
EILLE, Greg, R. [US/US]; 17636 Vineland Court, Monte
Serenio, CA 95037 (US).(74) Agents: CLAYTON, Grant, R. et al.; Thorpe, North & Western,
L.L.P., Suite 200, 9035 South 700 East, Sandy, UT 84070
(US).(81) Designated States: AL, AM, AT, AU, AZ, BA, BB, BG, BR,
BY, CA, CH, CN, CU, CZ, DE, DK, EE, ES, FI, GB, GE,
GH, HU, ID, IL, IS, JP, KE, KG, KP, KR, KZ, LC, LK,
LR, LS, LT, LU, LV, MD, MG, MK, MN, MW, MX, NO,
NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR,
TT, UA, UG, US, UZ, VN, YU, ZW, ARIPO patent (GH,
KE, LS, MW, SD, SZ, UG, ZW), Eurasian patent (AM, AZ,
BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE,
CH, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL,
PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN,
ML, MR, NE, SN, TD, TG).

Published

Without international search report and to be republished
upon receipt of that report.

(54) Title: SYSTEM AND METHOD FOR ADAPTING COMPUTER PERIPHERALS



(57) Abstract

A system and method are provided for interconnecting computer peripherals with portable and desktop computers and other digital devices. More specifically, the present invention includes a system and method for simultaneously connecting multiple portable computer peripherals to a single portable computer interface slot, for connecting portable computer peripherals to a desktop computer, and for connecting a portable ZIPTM drive to a portable computer. A system for providing a supplemental power supply for the computer peripherals is also provided.

SYSTEM AND METHOD FOR ADAPTING COMPUTER PERIPHERALS

BACKGROUND OF THE INVENTION

5

1. Field of the Invention

This invention pertains to systems and methods for interconnecting computer peripherals and computing devices. More specifically, the present invention provides a system and method for adapting one or more computer peripherals to a single computer interface slot and for providing an operative connection between the peripheral and the computing device.

10

2. State of the art

15

The present invention generally encompasses interfacing computer peripherals with computers. To understand the benefits derived from the embodiments of the invention to be described herein, it is helpful to understand the existing state of the art and to see why it fails to provide the advantages and benefits of the preferred and alternative embodiments of the present invention. It is also beneficial to a reader to consider the following materials which are herein incorporated by reference: U.S. patent application serial no. 08/399,728 filed on 07 March 1995 and PCT patent application serial no. PCT/US96/03248 filed on 07 March, 1996.

20

25

30

35

The first improvement of the present invention to be described generally pertains to portable computers, but has application with many other types of digital electronic devices.. Portable computers are generally understood to include the family of computers known as laptops, notebooks and portables. They are known as portables because they are generally smaller units compared to desktop computers which can more easily be carried from place to place and used at remote locations, often away from plug-in power sources such as AC power outlets. The benefits of mobile computing can be substantial. However, it is often the

case that the versatility of these portables is sacrificed for the sake of small size, lower power requirements, and generally any other considerations which make the computer portable. Therefore, peripheral components which are often a part of less easily portable desktop computer systems are not as easily integrated within a portable computer system. These same considerations also apply in the case of many other digital electronic devices.

Because space is limited within a chassis of a portable computer, and power constraints dictate that power drain must be kept to a minimum, most portable computing devices have only one bay which can be used to house only one of various peripheral devices at any given time. This bay is often referred to as a multi-function bay (also referred to herein as a peripheral bay or peripheral slot) when more than one type of peripheral can be inserted therein to communicate with the host portable computer. A floppy drive is often the default peripheral of choice to be placed in the bay because it is used nearly universally for data transfer. However, a CD-ROM drive has also become an almost indispensable peripheral for computers today. Therefore, when a CD-ROM drive is needed, the floppy drive is removed and the CD-ROM drive is inserted in its place, or swapped.

A significant drawback of the single multi-function bay portable computer described above is that only one peripheral device can be used at a time. Furthermore, it was also not mentioned that "hot-swapping", or the replacement of one peripheral device with another when the computer is operational, is typically not possible for floppy drives, CD-ROM drives and similar peripherals. Therefore, the portable computer has to be completely powered down before the swap can be made. When more than one peripheral component is needed or when the swapping of

two peripherals must be executed more than once, the cycle of powering down, swapping, and then powering up again can become tedious and wasteful of time.

Therefore, it would be an advantage over the prior art to provide a portable computer which can be simultaneously coupled to more than one computer peripheral which can be swapped in and out, and thus take advantage of the ability to have more than one of the computer peripherals electrically coupled to the portable computer, but without having to swap them. These advantages include the ability to have computer peripherals communicate directly with a computing device.

The next improvement of the present invention to be described pertains less specifically to the portable computer, and more to the portable computer peripherals which are swapped in and out of the single multi-function bay. Specifically, consider the situation where a portable computer is purchased with some portable computer peripherals. Typically, these portable computer peripherals are the only peripherals which will communicate with the portable computer. Notwithstanding the various reasons for this occurrence such as proprietary connectors, pinouts or timing schemes, the result is that portable computer peripherals will often only communicate with the portable computers for which they were specifically designed. Now consider that the same user also has a desktop computer. It is often the case that the same peripherals needed for the portable computer are also needed for the desktop computer. Consequently, the user is forced to buy the same peripherals for the desktop computer that were already purchased for the portable computer. The result is needless expense to the user because of redundancy in functionality of peripherals.

It would be an advantage over the prior art to be able to use the portable computer peripherals with the desktop computer despite the proprietary nature common to the computer peripherals.

5 Another improvement of the present invention to be described pertains more specifically to a portable computer and other portable computing devices. The desire within the industry to provide an easily portable and high density, removable mass storage medium contributed
10 significantly to the success of the ZIP™ drive manufactured by Iomega Corporation (hereinafter "Iomega"). The ZIP™ drive is comprised of a drive unit which stores data to removable ZIP™ disks. Slightly thicker than the ubiquitous three and one half inch 1.44 Megabyte floppies in use
15 today, one ZIP™ disk is nevertheless able to store about 100 Megabytes of data, or the storage capacity of roughly the equivalent of seventy 1.44 Megabyte floppies. The ability to store a large quantity of data or much larger single files on a single removable storage medium is very
20 advantageous. Consequently, desktop computers are beginning to include them as standard equipment which comes with a basic desktop computer system. However, portable computers have not been able to take advantage of removable ZIP™ drive capacity because of the interface requirements
25 of the multi-function bay of the portable computer, and the size constraints therein, and the power requirements. Therefore, it would be another advantage over the prior art to adapt a ZIP™ drive or other removable, high density storage medium for use with a portable computer or other
30 computing device to take advantage of inexpensive and removable mass storage capabilities.

In accordance with yet another improvement of the present invention, provision is made to provide additional current supplying capacity to a computer peripheral, such

as a ZIP™ drive, at times when the current supplied by the portable or desktop computer interface is not sufficient. For example, the current available under some interface standards may be less than that intermittently required by a computer peripheral, for example a ZIP™ drive. Embodiments of the present invention provide supplementary current to maintain the operation of the peripheral when necessary.

OBJECTS AND SUMMARY OF THE INVENTION

In view of the above described state of the art, the present invention seeks to realize the following objects and advantages.

It is an object of the present invention to provide a system and method for adapting and interconnecting computer peripherals and computer systems.

It is another object to provide a system and method for simultaneously connecting multiple computer peripherals to a single computer interface slot so that peripheral swapping is unnecessary.

It is still another object to provide a system and method for connecting any of a number of proprietary computer peripherals to a single computer interface slot.

It is yet another object to provide a system and method for connecting portable computer peripherals to a desktop computer so that these peripherals can be shared by both the portable computer and the desktop computer.

It is also an object to provide a system and method for connecting any of a number of proprietary portable computer peripherals to a desktop computer to reduce peripheral redundancy and thereby reduce the cost of peripheral systems to the user.

It is still another object to provide a system and method for connecting a drive using high density, portable and removable mass storage media to a portable computer so

that the portable computer can take advantage of convenient and inexpensive mass storage.

It is yet another object to provide a system and method as described above which uses a ZIP™ drive with removable ZIP™ disks as the high density, portable and removable mass storage media.

It is a further object of the present invention to provide a system and method for efficiently powering computer peripherals which intermittently require more power than is supplied by the interface standard used by the computer.

In accordance with these and other objects of the present invention, the advantages of the invention will become more fully apparent from the description and claims which follow, or may be learned by the practice of the invention to be described hereinafter.

The present invention provides a system and method for accomplishing the objective of adapting and interconnecting computer peripherals with portable and desktop computers and other digital computing devices. In a first aspect of the invention, a single interface slot on a computing device is electrically coupled to an interface port of an external docking bay comprised of a plurality of interface slots or ports. Portable peripherals are electrically coupled to the ports of the external docking bay. The computing device is then able to access all of the peripheral devices in the external docking bay which are normally only accessible one at a time when electrically coupled to a port in a multi-function bay made for receiving a single computer peripheral in the portable computer.

In a related aspect of the present invention, the single interface slot to which the external docking bay is electrically coupled is not necessarily the dedicated

multi-function port. In other words, a PC card slot (also known as a PCMCIA slot) can also be used to electrically couple the external docking bay to the portable computer.

Another related aspect of the present invention involves overcoming the proprietary characteristic of some interfaces between computer peripherals, particularly portable computer peripherals, and the computer, particularly portable computers and other computing devices. In other words, the present invention can be used where a variety of different proprietary interface standards are encountered.

Another aspect of the present invention is to provide an external docking bay which can be electrically coupled to a hard drive which is incompatible with the IDE interface or some other standard interface which is normally used by the portable computer or other computing device. This enables the external docking bay to act as an interface to the portable computer for devices which could not otherwise be coupled thereto.

Another aspect of the present invention is related to the advantageous capability of electrically coupling portable computer peripherals to a desktop computer or an expansion chassis. An internal docking bay is provided for insertion within a slot or port of the desktop computer. The internal docking bay is thereby electrically coupled to a communications bus within the desktop computer. The internal bay also provides a port which is compatible with an interface port of a portable computer peripheral. The desktop computer is thus able to interface with the portable computer peripherals as if they were inserted into the portable computer. The portable computer peripherals can still be swapped out of the desktop computer and into the portable computer when they are needed there.

Another aspect of the present invention is related to electrically coupling a specific desktop peripheral with a portable computer. Specifically, ZIP™ drives manufactured by Iomega are an increasingly popular removable, high density, mass storage device. However, no proprietary ZIP™ drives have been manufactured which will fit within the chassis of a portable computer. This is probably because a ZIP™ drive disk is significantly thicker than a compact disk (CD-ROM) or 1.44 Megabyte floppy. The present invention overcomes the challenge of electrically coupling a ZIP™ drive to a portable computer interface port such as a multi-function port within a multi-function bay.

Another aspect of the present invention is to provide a tape drive backup unit which is electrically coupled to the portable computer interface port within the multi-function bay.

Yet another aspect of the present invention is to provide a system and method for efficiently powering computer peripherals, such as ZIP™ drives, which intermittently require more power than is supplied an interface standard, such as the PCMCIA interface standard, used by computers.

These and other objects, features, advantages and alternative aspects of the present invention will become apparent to those skilled in the art from a consideration of the following detailed description taken in combination with the accompanying drawings.

DESCRIPTION OF THE DRAWINGS

Figure 1 is a perspective illustration of the external casing of a first external docking bay shown in an upright position and made in accordance with the principles of a preferred embodiment of a first aspect of the present invention with a second external docking bay shown in phantom image.

Figure 2 is an illustrative embodiment shown in perspective of the two external docking bays shown in figure 1, now electrically coupled to a portable computer via a PC card (PCMCIA) slot.

5 Figure 2A is a perspective view of a first exemplary peripheral device including a ZIP™ drive which can be used in accordance with the present invention.

 Figure 2B is perspective view of a second exemplary peripheral device including a ZIP™ drive which can be used
10 in accordance with the present invention.

 Figure 2C is a peripheral view of the exemplary peripheral device represented in figure 2A ready to be inserted into one of the external docking bays represented in figure 1.

15 Figure 2D is an exploded perspective view of the external peripheral device bay represented in figure 1.

 Figure 3 is a flow chart showing a preferred method of operation of the preferred embodiment of the present invention shown in figure 1.

20 Figure 4 is a modification to the flow chart of figure 3 which explains how the preferred embodiment of the present invention compensates for proprietary communications.

 Figure 5 is an illustration in perspective of an
25 external hard drive unit which is electrically coupled to the external docking bay 10 so as to be used by the portable computer with which it cannot typically communicate.

 Figure 6 illustrates another alternative embodiment of
30 the present invention which shows that portable computer peripherals can also be used on a desktop computer.

 Figure 7 illustrates another alternative embodiment of the present invention which enables a drive utilizing a portable, high density, removable storage medium to be

constructed to fit with the multi-function bay 36 and therefore be utilized as other portable computer peripherals.

Figure 8 is a block diagram showing another alternative embodiment of the present invention which accommodates one or more interfaces which are available in the industry.

Figure 9 is a block diagram of another embodiment of the present invention which includes a power management features.

Figure 10 is a detailed schematic diagram showing one preferred arrangement for implementing the power management feature represented in figure 9.

Figure 11 is a detailed schematic diagram showing one preferred arrangement for adapting a peripheral device to the PCMCIA standard.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS OF THE INVENTION

Reference will now be made to the drawings in which the various elements of the preferred embodiments of the present invention will be given numerical designations and in which the preferred embodiments of the invention will be discussed so as to enable one skilled in the art to make and use the invention.

A first aspect of the present invention is illustrated in figure 1. Figure 1 shows an external docking bay 10. A second, identical, external docking bay is also shown in phantom image in figure 1. The single external docking bay 10 pictured is designed to enable a single portable computer peripheral 12 to be electrically coupled to a portable computer 14 (see figure 2) or to some other computing device. As shown in figure 1, a stand 11, is provided to hold the external docking bay 10.

In an exemplary illustration, the portable computer 14 is a notebook computer manufactured and/or sold by such industry leaders in computer notebooks such as IBM Corporation ("IBM"), Compaq Computer Corporation ("Compaq") or Toshiba America Information Systems, Inc. ("Toshiba"). The external docking bay 10 is comprised of an insertion end, generally designated at 16, and also a bay door 17 where the portable computer peripheral 12 is inserted into the external docking bay 10. Within the external docking bay 10 at an opposing coupling end 18 is an interface port, diagrammatically represented at 20.

As shown best in figure 2, the portable computer peripheral 12 is electrically coupled to the interface port 20 because the interface port 20 is constructed to be substantially identical in physical dimensions and pinouts to a multi-function port 35 in the multi-function bay 36 of the portable computer 14. The interface port 20 is electrically coupled to a cable 22 which is also electrically coupled to a coupling device 24 shown partially inserted into the portable computer 14. The coupling device 24 (here configured as a PC card) enables the external docking bay 10 to be electrically coupled to the portable computer 14 at a portable computer interface port 30 (figure 2).

It should be noted that the external docking bay 10 is constructed to receive portable computer peripherals 12 which are designed to fit within a multi-function bay 36 of the portable computer 14 without modification. Therefore, the pertinent dimensions the external docking bay 10 are the same as those of a multi-function bay 36 in the portable computer 14.

It will also be appreciated that the two or more external docking bays 10 shown in figure 1 can be joined together as shown by the phantom image of an external

docking bay 10 in figure 1 and as shown in figure 2. Advantageously, a plurality of portable computer peripherals 12 can therefore be simultaneously coupled to the portable computer 14. One of the benefits of this configuration is that instead of having to swap portable computer peripherals 12 by shutting down the computer, replacing the current portable computer peripheral 12 with the desired portable computer peripheral 12, and then rebooting, all portable computer peripherals 12 are available without swapping. Moreover, the preferred embodiments of the present invention provide for "hot swapping" of the peripherals. The present invention also provides for management of the use of data communication lines within the cable 22 when two or more external docking bays 10 are coupled together. When necessary, the present invention provides communication management between two or more external docking bays 10 which is similar to bus arbitration techniques, as known to those skilled in the art, which determine which devices are able to use bus lines and when.

Still referred to figure 2, an illustration of an exemplary situation of the embodiment described in figure 1 will be provided. When a user has a floppy drive unit 28 presently installed in a multi-function bay 36 of the portable computer 14, this embodiment enables simultaneous use of another portable computer peripheral 12, for example a CD-ROM drive unit 32 which is inserted into the external docking bay 10. Normally, the user has no option but to remove the floppy drive unit 28 and install the CD-ROM drive unit 32 in the multi-function bay 36. But in this embodiment of the present invention, the external docking bay 10 is simultaneously coupled via a specific computer interface port. In this embodiment, a PC card slot 30 is used on the portable computer 14 as the interface between

the portable computer 14 and the external docking bay 10. Those skilled in the art will appreciate that any appropriate port can be used as the portable computer interface port 30. However, the PC card (PCMCIA) port 30 is an almost universally available interface standard on portable computers which facilitates implementation of this embodiment of the present invention. An added benefit of using the PC card slot 30 as the interface for the external docking bay 10 in this preferred embodiment is that the multi-function bay 36 of the portable computer 14 can still be used.

Two external docking bays 10 are shown coupled together in figure 2 so as to have the ability to simultaneously accept two computer peripherals 12. The CD-ROM drive unit 32 is inserted into the external docking bay 10 at the insertion end 16 until it is electrically coupled to an associated interface port 20 within.

For illustration purposes, the remaining interface port 20 within external docking bay 10 is shown having a tape drive backup unit 34 installed therein. However, those skilled in the art will appreciate that the tape drive backup unit 34 is optional. In other words, the interface ports 20 within the external docking bay 10 do not have to be filled for proper operation of the portable computer 14. One or both of the interface ports 20 can be empty when the portable computer 14 is booted. However, in some cases, to be able to use portable computer peripherals 12 other than the one installed in the multi-function bay 36, it may be necessary to install the portable computer peripherals 12 in the external docking bay 10 before booting the portable computer 14.

Assuming that the CD-ROM drive unit 32 and the drive backup unit 34 were installed in the external docking bay 10 before booting the portable computer 14, all three

portable computer peripherals 12 are now accessible by the portable computer 14. For example, data stored on a CD in the CD-ROM drive unit 32 can advantageously be copied directly to a floppy disk in the floppy drive unit 28.

5 Likewise, data stored in an internal hard disk 38 within the portable computer 14 can advantageously be copied directly to the tape drive backup unit 34 in the external docking bay 10. A portable computer running a multi-tasking operating system can even execute the data transfer and copying processes described above simultaneously. It is also preferred within the scope of the present invention that the peripherals which are accommodated by the external docking bays be "hot swappable."

15 Reference will next be made to figure 2A. Figure 2A is a perspective view of a peripheral device, generally designated at 40, in accordance with the present invention. The peripheral device 40 is a mass storage device which is compliant with the standard promulgated by Iomega and known in the industry as a ZIP™ drive and which receives a disk, designated at 42, known in the industry as a ZIP™ disk.

20 The peripheral device 40 is particularly configured to be received into the multi-function bay 36 (see figure 2) of a computer manufactured by Toshiba. The peripheral device 40 includes an enclosure or a housing 41. The housing 41 has a length 47, a width 45, and a thickness 46 all of which is specific to peripherals which are to be received into the multi-function bay 36 of computers complying with the pertinent standard promulgated by Toshiba. The housing 41 includes a lip 48 which extends from one side of the housing 41 and has a thickness 44 which is less than the thickness represented at 46. An electrical connector 49 is provided to make electrical connection with a corresponding electrical connector (represented at 35 in figure 2) provided within the multi-

function bay 36 of a computer complying with the multi-function bay standard of Toshiba or within the external docking bay 10 (figure 2) complying with the multi-function bay standard of Toshiba.

5 Reference will next be made to figure 2B. Figure 2B is a perspective view of another peripheral device, generally designated at 50, in accordance with the present invention. The peripheral device 50 is also a mass storage device which is compliant with the standard promulgated by Iomega and known in the industry as a ZIP™ drive which
10 receives a ZIP™ disk.

 The peripheral device 50 is particularly configured to be received into the multi-function bay 36 (see figure 2) which complies with the standard promulgated by IBM. The
15 peripheral device 50 includes an enclosure or a housing 57. The housing 57 has a length 56, a width 55, and a thickness 54, all of which is specific to peripherals which are to be received into the multi-function bay 36 of computers complying to the standard promulgated by IBM. The housing
20 57 includes a lip 53 which extends from one side of the housing 57 and has a thickness 51 which is less than the thickness represented at 54. An electrical connector 58 is provided to make electrical connection with a corresponding electrical connector (represented at 35 in figure 2)
25 provided within the multi-function bay 36 of a computer complying with the multi-function bay standard of IBM or within the external docking bay 10 (figure 2) complying with the multi-function bay standard of IBM.

30 Peripheral devices which comply with the multi-function bay standard of any number of different organizations and manufacturers can be arrived at using the teachings set forth herein. Among the steps to be carried out when arriving at a peripheral device in accordance with the present invention are: 1) Providing a housing for the

peripheral device which physically fits within the multi-function bay adhering to the pertinent standard; 2) Providing an electrical connector which is compatible with the corresponding electrical connector provided in the pertinent multi-function bay; and, 3) Providing a translation interface so that translation of communications between a first interface standard and a second interface standard can occur so that an operative link between the host computer and the peripheral is established and maintained. In accordance with the present invention, one skilled in the art can use these steps to arrive at peripheral devices which operate within the multi-function bays adhering to any number of different standards.

Reference will next be made to figure 2C. Figure 2C is a perspective view of the peripheral device 40 represented in figure 2A ready to be inserted into the external peripheral device bay represented in figure 1 as indicated by arrow 60. As will be appreciated by those skilled in the art, the peripheral device 40 is inserted and removed from the device bay in accordance with the standard promulgated by appropriate manufacturer or organization.

Figure 2D is an exploded perspective view of one external peripheral device bay 10 represented in figure 1. Those skilled in the art will appreciate that the same pertinent structures can be adapted for use internally to a computing device, such as a desktop computer.

As will now be appreciated from an understanding of the foregoing, the interface port 20 will change according to the standards promulgated by different manufacturers and different organizations. In the case of one preferred peripheral device, adhering to the standard promulgated by Toshiba, the interface port is a 50 conductor connector having the following pin assignments:

17

<u>Pin</u>	<u>Assignment</u>
1-40	ATAPI Interface
41, 42	+5 volts
43-44	GND
47	Audio Right
48	Audio GND
49	Audio Left
50	Audio GND

Represented in figure 2D are a first half housing 10A and a second half housing 10B. The first half housing 10A and the second half housing 10B are joined together using some of the screws indicated at 15 such that peripheral devices, such as the peripheral device 40 (figure 2A) and the peripheral device 50 (figure 2B) can be received therein. In particular, the arrangement illustrated in figure 2d can securely receive and hold a peripheral device and the arrangement illustrated in figure 2D is particularly configured to receive peripherals complying with the standards promulgated by Toshiba. A circuit board 13 includes the interface port 20 and a cable connector 21 which receives cable 22. A spring 19 is included to make a ground connection with the peripheral device in accordance with the standard promulgated by Toshiba. Some of screws 15 are used to secure the structures together.

From the forgoing, it will be appreciated that docking bays which can be used with, and which comply with, the peripheral standards of any number of different organizations and manufacturers can be arrived at using the teachings set forth herein. Among the steps to be carried out in accordance with the present invention when arriving at a peripheral device are: 1) Providing a housing for the peripheral docking bay which physically receives the peripheral; 2) Providing an electrical connector in the peripheral docking bay which is

compatible with the corresponding electrical connector provided on the peripheral; and, 3) Providing a translation interface so that translation of communications between a first interface standard and a second interface standard can occur so that an operative link between the host computer and the peripheral is established and maintained. In accordance with the present invention, one skilled in the art can use these steps to arrive at docking bays which receive peripherals which comply with any number of different standards.

Figure 3 is a flow chart showing one general method of operation of the preferred embodiment illustrated in figure 1. It should be remembered that this illustration is only a preferred embodiment and can therefore be implemented using other methods within the scope of understanding of those skilled in the art after a presentation of the nature of the present invention as taught herein.

Figure 3 begins with step 80 where all desired portable computer peripherals 12 are installed in an external docking bay 10 having an appropriate number of bays. It should be mentioned that although it was explained earlier that none or as a few as one of the external docking bays 10 need to be filled for the portable computer 14 to operate, the same is true for the multi-function bay 36 of the portable computer 14. The multi-function bay 36 can be empty and the operation of the portable computer 14 will not be affected.

Step 82 requires that the portable computer 14 be rebooted. The portable computer peripherals 12 are now all simultaneously available to the user. In the preferred embodiment, the operating system and/or a hardware component such as a bus controller of the portable computer 14 assigns a unique drive designation to each of the portable computer peripherals 12 in step 84 so that they can be accessed accordingly. For illustration purposes only, the portable

computer peripheral 12 in the multi-function bay could always be assigned the designation "C:", the first peripheral in the external docking bay 10 could always be assigned the designation "D:", and so forth until the last portable computer peripheral 12 in the external docking bay 10 is assigned a drive designation. If there is no portable computer peripheral 12 in the multi-function bay 36, then the first portable computer peripheral 12 in the external docking bay 10 will receive the drive designation "C:" and so on as before.

Step 86 in the preferred embodiment encompasses the concept of deciding which of the portable computer peripherals 12 the portable computer 14 is going to communicate with, and when. For example, in the preferred embodiment, an interrupt-type method of communication is chosen. When a portable computer peripheral 12 requires communication with the portable computer 14 or another portable computer peripheral 12, an interrupt can be set which designates the appropriate portable computer peripheral 12 as requiring attention for the portable computer 14.

Step 88 shows that in the preferred embodiment, once the portable computer 14 has determined that one of the portable computer peripherals 12 requires attention, a predetermined amount of time is devoted to responding to the portable computer peripheral's 12 task. The predetermined amount of time might require repeated interruptions of the task while other functions of the portable computer 14 are carried out. Those skilled in the art will appreciate that the function described can be executed in many different ways. However, the portable computer 14 and its operating system can be assumed to already possess the ability to control a plurality of portable computer peripherals 12 already. This should be clear in that the functionality added is an extension of the portable computer's 14 ability to share bus time and perform

other task for an internal hard drive 38 and floppy drive unit 28 which are already typically installed in the portable computer 14 (see figure 2).

Line 90 is also included to indicate that the portable computer 14 is always looping back to check on a status of other portable computer peripherals 12, if any, after a task is completed, or even during a task if the portable computer 14 and operating system enable such an interruption. It will be appreciated by those skilled in the art that peripheral device status checking is accomplished by numerous methods, and are considered to be within the scope and teachings of the present invention.

In an alternative embodiment, the impetus for communication between the portable computer peripherals 12 and the portable computer 14 in step 84 can be replaced with a polling scheme. The portable computer 14 will actively poll all portable computer peripherals in a rotating fashion, basically asking each peripheral if it requires communication. If not, the portable computer 14 queries a next portable computer peripheral 12 in an endless loop. The loop is only interrupted when a portable computer peripheral 12 requires attention, and then perhaps only in small segments of time until the portable computer peripheral 12 no longer requires attention from the portable computer 14 when its task is complete.

Figure 4 shows an additional step 92 which is inserted between the steps 82 and 84 in an alternative embodiment of the present invention. In this embodiment, the portable computer 14 makes a determination upon bootup about which type of portable computer peripherals 12 are electrically coupled to the external docking bay 10. While the interface port 20 may have the same arrangement of pins for the many different models or computer manufacturers of portable computers 14, the signals on the pins may differ significantly. Therefore, an

alternative embodiment of the present invention is the capability to determine at an appropriate time what type of computer peripheral 12 the portable computer 14 will be communicating with. In this embodiment, the external docking bay 10 can then make the appropriate adjustments to compensate for changes in signal assignments on pins of various portable computer peripherals 12. It is also envisioned that some portable computer peripherals 12 use the same pinout configurations, but that signal timing is altered. Therefore, the present invention is prepared to compensate for these timing differences or pinout variations by an appropriate method known to those skilled in the art. For example, a signal which must be generated on a particular line for a certain length of time, as required by the portable computer 14, can be extended by the external docking bay 10 if the portable computer peripheral 12 otherwise drops the signal too early. Likewise, the external docking bay 10 can also cut signal short in the same manner.

It is to be appreciated that the steps described in connection with figures 3 and 4 are merely exemplary and that many alternative procedures can be used in accordance with the present invention.

Figure 5 shows that an external hard drive unit 90 is electrically coupled to the external docking bay 10. There are some hard drives which do not conform to typical bus interfaces because they are much larger than a typical hard drive. The external docking bay 10 can be used as an interface between the bus of the portable computer 14 and the hard drive 90. It should be appreciated by those skilled in the art that another advantage of the present invention therefore is the ability to function as an interface to the portable computer 14 for peripherals which are not normally compatible.

Figure 6 illustrates another alternative embodiment of the present invention which shows that portable computer peripherals 12 can also be used on a desktop computer 100, and thereby avoid duplicate but otherwise necessary and expensive proprietary purchases to outfit both a portable computer 14 and the desktop computer 100 with the same peripherals. It should be realized that this embodiment has application for use in an expansion chassis (not shown) as well.

Figure 6 shows that an internal docking bay 102 is electrically coupled to an internal bus slot 104 of the desktop computer 100 by any appropriate means such as direct connection or via a cable, and shown here in a tower or mini-tower configuration. The bus used by the desktop 100 can be any of those commonly used today such as ISA, EISA, Microchannel, VESA, and PCI and which are known to those skilled in the art. Similarly, ATAPI, PCMCIA, parallel, serial, SCSI or FireWire peripheral connections might also be used to couple the internal docking bay 102 to the desktop computer 100. The internal docking bay 102 provides an interface between the signals being used by the computer peripheral 12, and the industry standardized bus signals used by the desktop computer 100. It is envisioned that an internal docking bay 102 would be purchased for using the computer peripherals 12 of only a particular computer manufacturer. All of the computer peripherals 12 would then be able to function on the desktop computer 100 from within the internal docking bay 102. Alternatively, more than one internal docking bay 102 can be included in the desktop computer 100, each internal docking bay 102 adhering to a different interface standard.

One benefit that is immediately realized from this described embodiment is the advantage of using the same computer peripherals 12 with the desktop computer 100 and the portable computer 14.

Next, figure 7 is provided to illustrate that the present invention also teaches installation of a drive 110 which utilizes an easily portable, high density, removable storage medium. An example of such a drive and storage medium are the ZIP™ drive as disclosed previously. The ZIP™ drive 110 has been constructed such that it is able to fit within the multi-function bay 36 of the present invention. Likewise, the ZIP™ drive 110 can be used in the external docking bay 10 (figure 1) and the internal docking bay 102 (figure 6) of previously described embodiments.

In accordance with another aspect of the present invention, the present invention provides that one or more industry standard interfaces can be accommodated. Represented diagrammatically in figure 8 is a computer device 118 as is known in the industry or which may become available in the future. The computing device 118 can include one or more industry standard interfaces such as those known as PCI, PCMCIA, CardBus, IEEE 1284 and/or FireWire (IEEE 1394). The hardware and/or software necessary to implement these industry standards is represented by the interface 120 in figure 8.

The signals which are conveyed by the interface 120 are communicated by a link 116 which may be a hardwired link or a wireless link, in accordance with which the industry standard interface 120 adheres. Wireless links within the scope of the present invention can be those which adhere to an industry standard infrared communication protocol or to a proprietary infrared communication protocol. Furthermore, wireless links which adhere to an industry standard radio frequency protocol or to a proprietary radio frequency communication protocol can also be used within the scope of the present invention.

Represented in figure 8 is an adaptable docking bay 114 which preferably substantially includes the features and structures described in connection with docking bay 10 described earlier. The adaptable docking bay 114 also

includes an adaptable interface which can accommodate any one, or in some preferred embodiments, more than one, industry standard interface such as ISA, PCI, PCMCIA, CardBus, and/or FireWire so that reliable and efficient communication between the adaptable docking bay 114 and the computing device 118 can occur. More information on the FireWire standard can be obtained from the IEEE 1394 standard and the documents which have been promulgated by the IEEE under the 1394 standard are hereby incorporated herein by reference.

As explained above, the docking bay 10 receives one of a number of different peripherals. In the case of the adaptable docking bay 114, a peripheral 124 is received into the adaptable docking bay 114. The peripheral 124 may be any number of different types of devices, such CD-ROM drives, disk drives, and many other different types of devices. Such devices may require different industry standard interface. Thus, the peripheral 124 may require an interface such as an IDE interface, an ATAPI interface, FireWire interface, SCSI interface, or another interface.

Represented in figure 8 is an interface adapter represented at 122. The interface adaptor 122 includes the hardware and/or software which is necessary to allow data and instructions to be transferred between two dissimilar standards. For example, the interface adapter 122 allows communication to efficiently occur between one or more of the following standards, all of which are well known in the industry: PCCard, PCI, Parallel, IEEE 488, Serial, RS-232, PS/2, PCMCIA, CardBus, FireWire, IDE, ATAPI, or some proprietary interface which one or more particular manufacturers have adopted or may adopt in the future. Thus, it will be appreciated that if the peripheral 124 requires an IDE/ATAPI interface, the interface adaptor 122 can preferably provide adaptable communication in accordance with the FireWire interface, if that is the standard with which the

computing device 118 is equipped. It is to be understood that the present invention can be used with many different types of computing devices, not just the computing device 118 represented in figure 8.

5 The preferred embodiment and the alternative embodiments of the present invention teach a system and method for interconnecting computer peripherals with both a desktop computer and a portable computer, where the peripherals 12 can be used on both computers 14 (figure 2) and 100 (figure 6) and
10 other computing devices. Furthermore, the present invention also teaches that a portable computer can be coupled to more than computer peripheral 12 even though the portable computer has a single multi-function bay, thus eliminating swapping of portable computer peripherals. The present invention is also
15 able to accommodate various proprietary protocols.

Reference will next be made to figure 9 which is a block diagram illustrating another aspect of the present invention. As discussed above, many computing peripherals have not been designed considering portability. Many computing peripherals
20 are designed to operate assuming that a high current source is available to operate the peripheral. The present invention advantageously provides a system and method for providing power to peripherals which provides advantages not previously available.

25 Figure 9 provides a block diagram of including a computer indicated at 150. As intended herein, the computer 150 can be any device which stores, manipulates, or utilizes data, for example, portable computers, desktop computers, personal digital assistants (PDAs), still digital cameras, video
30 cameras, and so forth. The computer 150 includes a PC card slot 152 which is compliant with the pertinent PCMCIA standard, as is well known in the industry. A PCMCIA device 154 is inserted into the PC card slot 152 to provide an interface between the computer 150 and a peripheral device 156

so that operation and communication can occur therebetween. As is known in the industry, the PCMCIA standard requires that five volts be provided at the PC card slot 152 and the standard requires that only 800 milliamps be provided.

5 As explained earlier, the PCMCIA device preferably provides an interface between the standard used by the peripheral device 156 (for example the ATAPI standard) and the standard used by the computer 150 (for example the PCMCIA standard). The PCMCIA device 154 passes through the five
10 volts to the peripheral device 156.

Many peripheral devices nominally draw less than 800 mA during operation. Nevertheless, intermittently during the peripheral's operation, the peripheral may draw more than 800 mA. During times when the peripheral draws more than 800 mA,
15 the PCMCIA standard does not assure that the current provided via the PC card slot 152 will be great enough to properly operate the peripheral. Thus, a power management unit 158 is provided in accordance with the present invention.

The power management unit 158 can be housed in the same
20 enclosure, represented by the dashed line 162, as the peripheral device 156, or can be separately housed. Also represented in figure 9 is a battery 160. The battery 160 can be any power storage device and is preferably a rechargeable electrochemical cell. For example, the battery 160 can
25 comprise one or more nickel-cadmium cells, one or more nickel-metal-hydride cells, or one or more lithium-ion cells. The battery 162 is also preferably housed within the enclosure 162. The enclosure 162 can preferably be the housing of the external docking bay 10 (figure 1) or the housing of the ZIP™
30 drive 110 which fits within the multi-function bay 36 (figure 7). Alternatively, the battery 160 and/or the power management unit 158 can be located outside of the enclosure 162.

The power management unit 158 monitors the current which is drawn by the peripheral device 156 from the PC card slot 152 through the PCMCIA device 154. As the current drawn by the peripheral device 156 nears and exceeds a predetermined threshold, the power management unit 158 draws current from the battery 160 to supplement the current drawn from the PC card slot 152 so the peripheral device 156 will have adequate current for proper operation. It is also within the scope of the present invention for the power management unit to recharge the battery 160 when the current drawn by the peripheral device 156 falls below the predetermined threshold. The battery 160 can also preferably be charged via an external source, such as a power supply connected to an AC current supply (not represented in the figures).

In the preferred embodiment of the present invention described herein, the predetermined threshold is preferably in the range from about 500 mA to about 800 mA, more preferably about 600 mA to about 700 mA, and most preferably is 650 mA. It will be appreciated that the predetermined threshold can be different than that specified herein and still fall within the scope of the present invention. It will be appreciated that the present invention provides advantages not heretofore available in the art.

Reference will next be made to figure 10 which is a detailed schematic diagram showing one preferred arrangement for implementing the power management feature represented in figure 9. It is to be appreciated that the detailed schematic diagram of figure 10 is not to be considered limiting of the scope of the present invention but is to be considered merely exemplary of the many different embodiments which can incorporate the present invention. The components represented in figure 10 correspond to those components which carry out the functions of the battery (160 in figure 9) and the power management unit (158 in figure 9). For purposes of clarity,

the reference designations which are customarily included in detailed schematic diagrams have been retained. Table A, below, provides a description of the preferred components represented in Figure 10.

TABLE A Figure 10	
<u>Reference Designation</u>	<u>Description</u>
BT1	Nickel-Cadmium Battery
F1	Fuse
R1	470K
R3	100K
U1A	LM393
R2	4.7K
D1	LED
R4	400K
R5	100K
D2	1N5235A
Q1	MTP12P10
JP1	Power connector to peripheral device
R7	.1
R8	82.5K
R9	1K
U1B	LM393
R6	4.7K

Upon examination of figure 10 it will be appreciated that the circuit represented therein is just one example of a means for detecting when the current drawn by the peripheral device exceeds a predetermined threshold and for supplying supplementary current to the peripheral device. It is to be understood that many different structures can carry out the functions of detecting when the current drawn by the

peripheral device exceeds a predetermined threshold and for supplying supplementary current to the peripheral device and all such structures and arrangements which perform the same or equivalent functions are to be considered within the scope of the means for detecting when the current drawn by the peripheral device exceeds a predetermined threshold and for supplying supplementary current to the peripheral device.

In view of the forgoing, it will be appreciated that the power management aspect of the present invention has utility with many different types of peripheral devices, both those which are presently available and which will become available in the future. An example of a peripheral device which will shortly become available is the n•hand disk drive which has been announced by Iomega which will use magnetic disks which are smaller than ZIP™ disks (current specification 2 inches by 2 inches) and which are expected to hold 20 MB of data and which are suited for applications which currently require flash memory cards.

The n•hand device is particularly suited for inclusion in apparatus such as Personal Digital Assistants (PDAs) and other miniature digital electronic devices. Devices such as PDAs are often designed to consume very little power in order to maximize battery life while using small battery cells. Thus, such devices may only be able to provide as little as, for example, 200 mA, 100 mA, or 50 mA to a peripheral device. Devices such as PDAs may comply with the PCMCIA interface standard except for being unable to provide the current which is otherwise expected under the PCMCIA interface standard. Those skilled in the art will readily be able to modify the structures disclosed herein, and to arrive at new structures using the teachings presented herein, to provide power management for peripheral devices which could not otherwise be reliably and conveniently used with low power digital electronic apparatus. Thus, depending upon the peripheral

device and the apparatus to which it is connected, the capacity of the battery (or other power storage component) shown in figure 10 may be altered as well as altering the threshold at which supplementary current provided to the peripheral and how much current is provided to the peripheral.

In view of the new peripherals and technologies which will be introduced in the future, it is to be understood that systems providing the power management features just described may be different than that represented in figure 10 but yet still fall within the scope of the present invention. In particular, the voltage and current values which are applicable with other peripheral devices may be different than those specified herein yet still fall within the scope of the present invention.

Figure 11 is a detailed schematic diagram showing one preferred arrangement for adapting a peripheral device to the PCMCIA standard. It is to be understood that the schematic diagram of figure 11 is not to be considered limiting of the scope of the present invention but is to be considered merely exemplary of the many different embodiments which can incorporate the present invention. The components represented in figure 11 carry out the functions of the coupling device 24 represented in figure 2 and the components represented in figure 11 can most preferably be housed within a PC card format.

The schematic diagram of figure 11 is only one example of a means for translating communications between a computer in accordance with the first interface standard and the peripheral device in compliance with the second interface standard such that an operative link between the computer and the peripheral device is established when a computer peripheral is nested in the peripheral bay. It is to be understood that many different structures can carry out the functions of means for translating communications between a

computing device in accordance with the first interface standard and the peripheral device in compliance with the second interface standard and all such structures and arrangements which perform the same or equivalent functions are to be considered within the scope of the means for translating communications between a computer in accordance with the first interface standard and the peripheral device in compliance with the second interface standard.

The arrangement represented in the detailed schematic diagram of figure 11 is most preferred for translating between the well known ATAPI interface standard and the PCMCIA interface standard. As can be appreciated from an examination of the detailed schematic diagram of figure 11, the arrangement represented in figure 11 does not require any active components to provide the desired translation in accordance with the present invention. It is also within the scope of the present invention to provide means for translating communications which includes active components which can modify existing signals, and create new signals, which are necessary to translate between interface standards. For example, those skilled in the art can arrive at means for translating communications between any of following interface standards: PCCard, PCI, Parallel, IEEE 488, Serial, RS-232, PS/2, PCMCIA, CardBus, FireWire, IDE, ATAPI interfaces; using the teachings provided herein, all of which fall within the scope of the present invention.

In view of the forgoing, it will be appreciated that the present invention provides a system and method for interconnecting computer peripherals and computer systems and for simultaneously connecting multiple portable computer peripherals to a single portable computer interface slot so that peripheral swapping is unnecessary. The present invention also provides a system and method for connecting any of a number of proprietary portable computer peripherals to a

single computer interface slot, for connecting portable computer peripherals to a desktop computer so that these peripherals can be shared by both the portable computer and the desktop computer, and for connecting any of a number of
5 proprietary portable computer peripherals to a desktop computer to reduce peripheral redundancy and thereby reduce the cost of peripheral systems to the user.

The present invention also provides a system and method for connecting a drive using high density, portable and
10 removable mass storage media to a portable computer so that the portable computer can take advantage of convenient and inexpensive mass storage, and specifically provide a system and method which uses a ZIP™ drive with removable ZIP™ disks as the high density, portable and removable mass storage
15 media. The present invention also provides a system and method for efficiently powering the computer peripherals.

It is to be understood that the above-described embodiments are only illustrative of the application of the principles of the present invention. Numerous modifications
20 and alternative arrangements may be devised by those skilled in the art without departing from the spirit and scope of the present invention. The appended claims are intended to cover such modifications and arrangements.

CLAIMS

What is claimed is:

1. A system for providing power to a digital peripheral device when a primary power supply provides insufficient power, the system comprising:

means for removably housing the peripheral device, the peripheral device including at least a first electrical connector for communicating at least a first signal;

means for coupling the first electrical signal between the peripheral device and a computing device, the computing device including a primary power supply;

means for conveying electrical power from the primary power supply to the peripheral device;

means for storing electrical power, the means for storing electrical power positioned outside of the computing device;

means for detecting when the current drawn by the peripheral device exceeds a predetermined threshold and for supplying supplementary power to the peripheral device from the means for storing electrical power when the current drawn by the peripheral device exceeds the predetermined threshold such that operation of the peripheral device is maintained.

2. A system as defined in claim 1 wherein the means for removably housing the peripheral device comprises at least one selected from the group consisting of: and external docking bay and an internal docking bay.

3. A system as defined in claim 2 wherein the computing device comprises one selected from the group consisting of: a desktop computer, a notebook computer, a palmtop computer, a personal digital assistant, a digital camera; and wherein the peripheral device comprises one

selected from the group consisting of: a magnetic storage device and an optical storage device.

4. A system as defined in claim 1 wherein the means for coupling the first electrical signal between the peripheral device and the computing device comprises a cable.

5. A system as defined in claim 1 wherein the power supply provides 5 volts DC.

6. A system as defined in claim 1 wherein the means for conveying electrical power from the primary power supply to the peripheral device comprises a cable.

7. A system as defined in claim 1 further comprising an enclosure within which the peripheral device is housed and wherein the means for storing electrical power comprises a rechargeable battery positioned within the enclosure.

8. A system as defined in claim 1 further comprising an enclosure within which the peripheral device is housed and wherein the means for storing electrical power comprises a rechargeable battery positioned outside of the enclosure.

9. A system as defined in claim 1 wherein the means for detecting when the current drawn by the peripheral device exceeds a predetermined threshold comprises means for detecting when the current drawn by the peripheral device exceeds a current value in the range from about 600 mA to about 700 mA.

10. A system as defined in claim 1 wherein the means for detecting when the current drawn by the peripheral device exceeds a predetermined threshold comprises means for detecting when the current drawn by the peripheral device exceeds a current value in the range from about 50 mA to about 200 mA.

11. A system as defined in claim 1 wherein the means for detecting when the current drawn by the peripheral device exceeds a predetermined threshold comprises means for

detecting when the current drawn by the peripheral device exceeds a current value equal to about 650 mA.

12. A system as defined in claim 1 wherein the means for detecting when the current drawn by the peripheral device exceeds a predetermined threshold and for supplying
5 supplementary power to the peripheral device comprises:

a first operational amplifier having an input connected to a first power input of the peripheral device for sensing the current drawn by the peripheral device;
10 and

a switching device in series with the means for storing electrical power and connected to a second power input of the peripheral device.

13. A system as defined in claim 1 wherein the first
15 electrical connector comprises a forty conductor connector.

14. A system for electrically coupling a peripheral device to a computing device, the computing device having an interface compliant with a first interface standard, said system comprising:

5 a first peripheral bay comprising means for making physical and electrical connection with the peripheral device, the peripheral device being compliant with a second interface standard; and

10 means for translating communications between the computer in accordance with the first interface standard and the first peripheral bay in compliance with the second interface standard such that an operative link between the computer and the computer peripheral is established when a computer peripheral is nested in the
15 first peripheral bay.

15. The system as defined in claim 14 wherein the first peripheral bay is positioned external to the computing device.

16. The system as defined in claim 14 wherein the first peripheral bay is positioned internally in the computing
20 device.

17. The system as defined in claims 15 or 16 wherein the first peripheral bay comprises:

 a housing;

25 an opening in the housing which receives the peripheral device into the housing, including a lip provided on the peripheral device enclosure.

18. The system as defined in claims 15 or 16 further comprising a second peripheral bay operatively connected to the computing device.

30 19. The system as defined in claims 15 or 16 wherein the means for making physical and electrical connection with a computer peripheral comprises a fifty conductor connector and a cable.

20. The system as defined in claims 15 or 16 wherein the computer peripheral comprises an enclosure having first, second, third, and fourth sides, the enclosure having a substantially rectangular shape having a lip extending from the first side thereof.

21. The system as defined in claims 15 or 16 wherein the first interface standard is selected from the group consisting of: PCCard, PCI, Parallel, IEEE 488, IEEE 1284, Serial, RS-232, PS/2, PCMCIA, CardBus, FireWire, IDE, ATAPI interfaces.

22. The system as defined in claims 15 or 16 wherein the second interface standard is selected from the group consisting of: PCCard, PCI, Parallel, IEEE 488, Serial, RS-232, PS/2, PCMCIA, CardBus, FireWire, IDE, ATAPI interfaces.

23. A system for electrically coupling a first peripheral device to a computing device having an enclosure, the computing device having an interface compliant with a first interface standard and the first peripheral having an interface compliant with a second interface standard, said system comprising:

a first peripheral bay, the peripheral bay comprising:

a housing, the housing positioned outside of the enclosure of the computing device;

an opening at one end of the housing for receiving the first peripheral device into the first peripheral bay and for securing the peripheral device in the first peripheral bay;

an interface connector positioned in the first peripheral bay, the interface connector coupling with a corresponding connector provided on the peripheral device and receiving signals from the peripheral in compliance with the second interface standard;

means for conveying the signals from the peripheral bay to the computing device; and

means for translating the signals peripheral device in the first peripheral bay which are in compliance with the second interface standard to the first interface standard used by the computing device such that an operative link between the computer and the peripheral device is established when a computer peripheral is received in the first peripheral bay.

24. A system as defined in claim 23 wherein the means for conveying the signals from the peripheral bay to the computing device comprises a cable.

25. A system as defined in claim 23 wherein the means for conveying the signals from the peripheral bay to the

computing device comprises means for creating and maintaining an infrared link.

26. A system as defined in claim 23 wherein the means for conveying the signals from the peripheral bay to the computing device comprises means for creating and maintaining a radio frequency link.

27. The system as defined in claim 23 wherein the first interface standard is selected from the group consisting of: PCCard, PCI, Parallel, IEEE 488, IEEE 1284, Serial, RS-232, PS/2, PCMCIA, CardBus, FireWire, IDE, ATAPI interfaces.

28. The system as defined in claim 27 wherein the second interface standard is selected from the group consisting of: PCCard, PCI, Parallel, IEEE 488, Serial, RS-232, PS/2, PCMCIA, CardBus, FireWire, IDE, ATAPI interfaces.

29. A system for adapting a peripheral device compliant with a second interface standard for operation with a computing device compliant with a first interface standard, said system comprising:

5 means for removably housing the peripheral device, the peripheral device including at least a first electrical connector for communicating at least a first electrical signal;

10 means for coupling the first electrical signal between the peripheral device and a computing device, the computing device including a power supply;

means for conveying electrical power from the power supply to the peripheral device;

15 means for storing electrical power, the means for storing electrical power positioned outside of the computing device;

20 means for detecting when the current drawn by the peripheral device exceeds a predetermined threshold and for supplying supplementary power to the peripheral device from the means for storing electrical power when the current drawn by the peripheral device exceeds the predetermined threshold such that operation of the peripheral device is maintained; and

25 means for translating communications between the computer in accordance with the first interface standard and the peripheral device in compliance with the second interface standard such that an operative link between the computer and the peripheral device is established when a computer peripheral is nested in the first peripheral bay.

30 30. A system as defined in claim 29 wherein the means for removably housing the peripheral device comprises at least one selected from the group consisting of: an external docking bay and an internal docking bay.

31. A system as defined in claim 30 further comprising a cable communicating the first electrical signal to the computing device.

32. A system as defined in claim 29 wherein the power supply provides 5 volts DC.

33. A system as defined in claim 29 wherein the means for means for conveying electrical power from the power supply to the peripheral device comprises a cable.

34. A system as defined in claim 29 further comprising an enclosure within which the peripheral device and wherein the means for storing electrical power comprises a rechargeable battery positioned within the enclosure.

35. A system as defined in claim 29 further comprising an enclosure within which the peripheral device and wherein the means for storing electrical power comprises a rechargeable battery positioned outside of the enclosure.

36. A system as defined in claim 29 wherein the means for detecting when the current drawn by the peripheral device exceeds a predetermined threshold comprises means for detecting when the current drawn by the peripheral device exceeds a current value in the range from about 600 mA to about 700 mA.

37. The system as defined in claim 29 wherein the first means for removably housing the peripheral device is positioned externally to the computing device.

38. The system as defined in claim 29 wherein the first peripheral bay is positioned internally in the computing device.

39. The system as defined in claims 37 or 38 wherein the means for making physical and electrical connection with a computer peripheral comprises a fifty conductor connector and a cable.

42

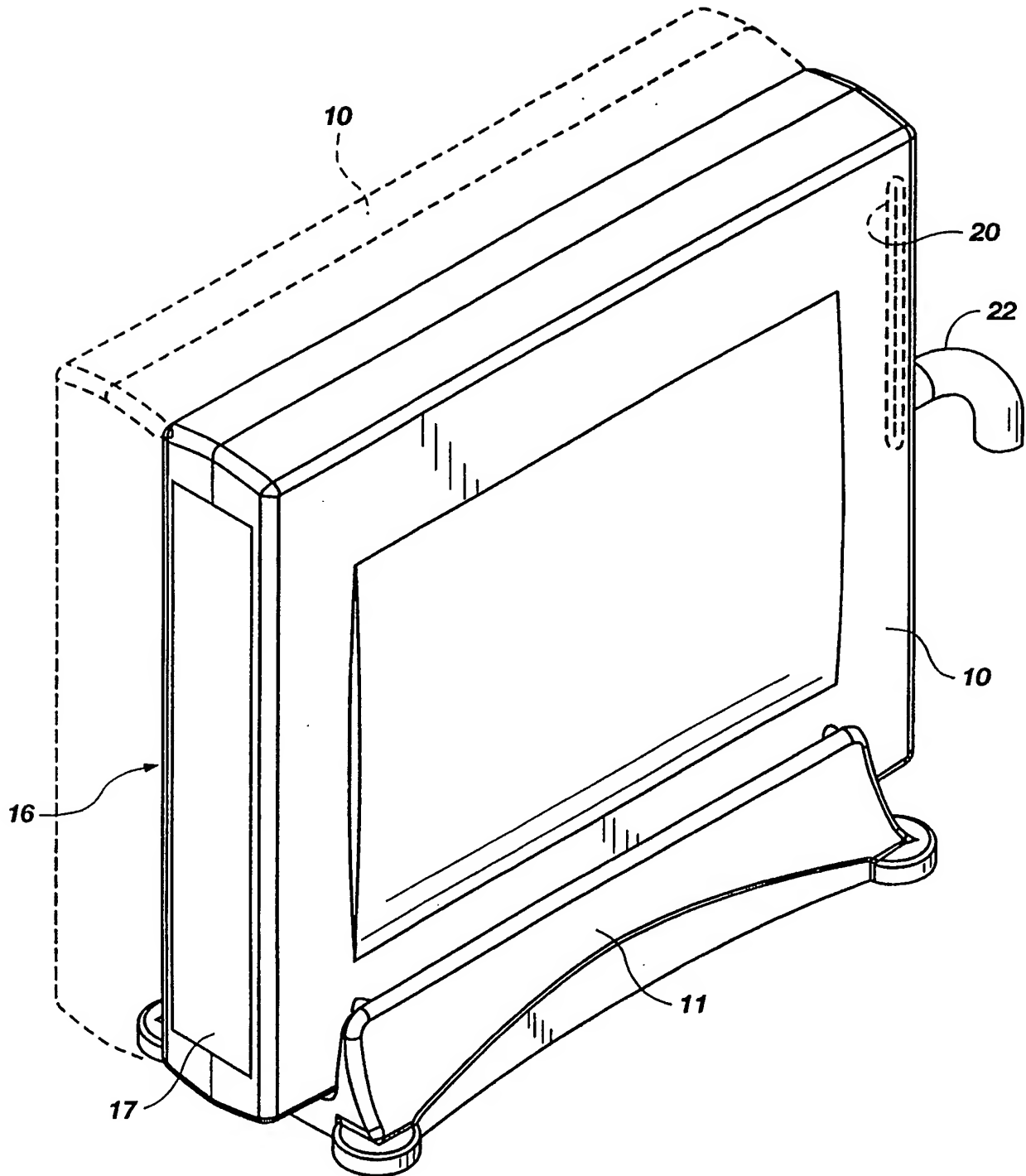
40. The system as defined in claims 37 or 38 wherein the computer peripheral comprises an enclosure having first, second, third, and fourth sides, the enclosure having a substantially rectangular shape having a lip extending from the first side thereof.

5

41. The system as defined in claims 37 or 38 wherein the first interface standard is the PCMCIA interface standard.

42. The system as defined in claim 41 wherein the second interface standard is ATAPI interface standard.

1/12

**Fig. 1**

2/12

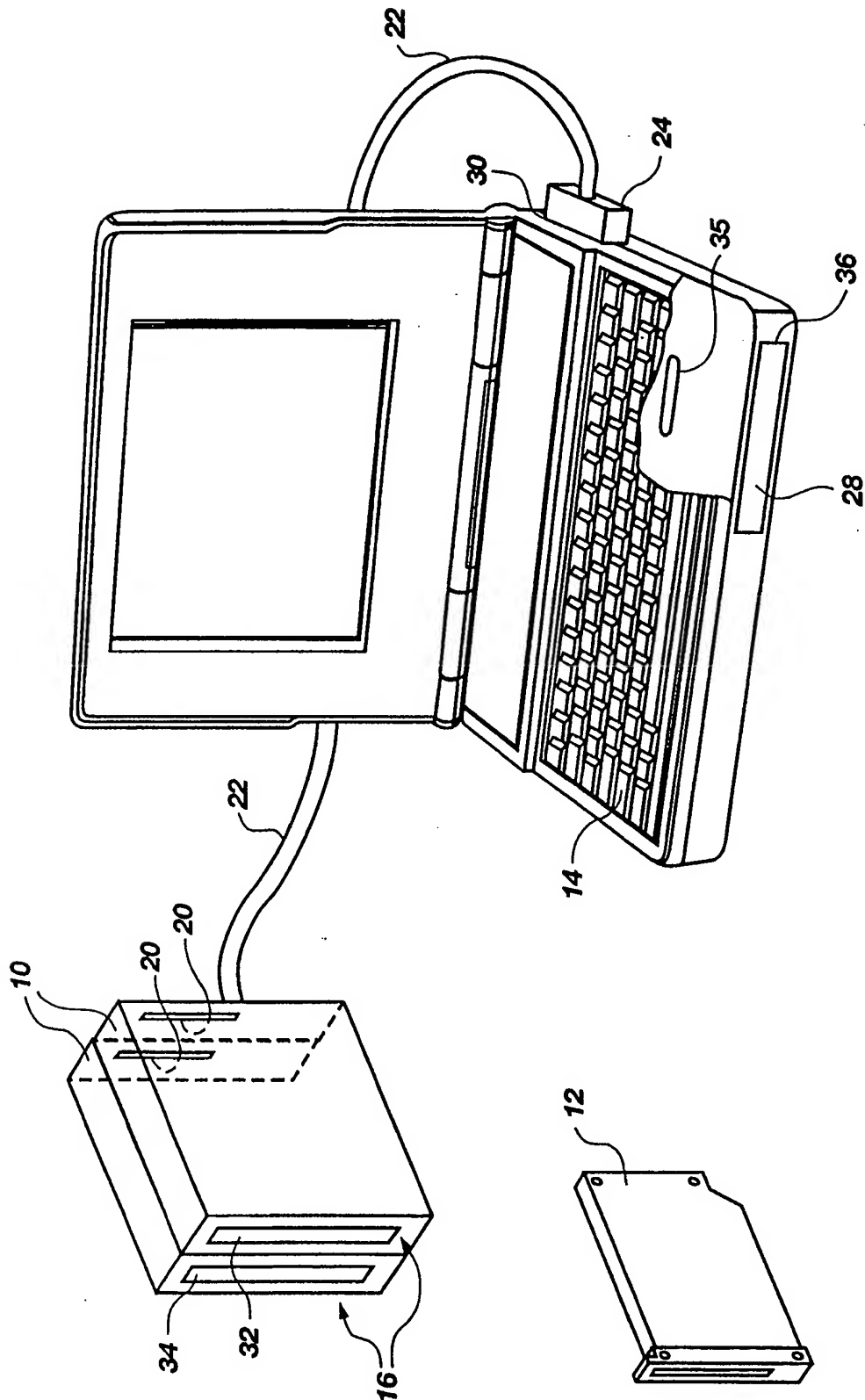
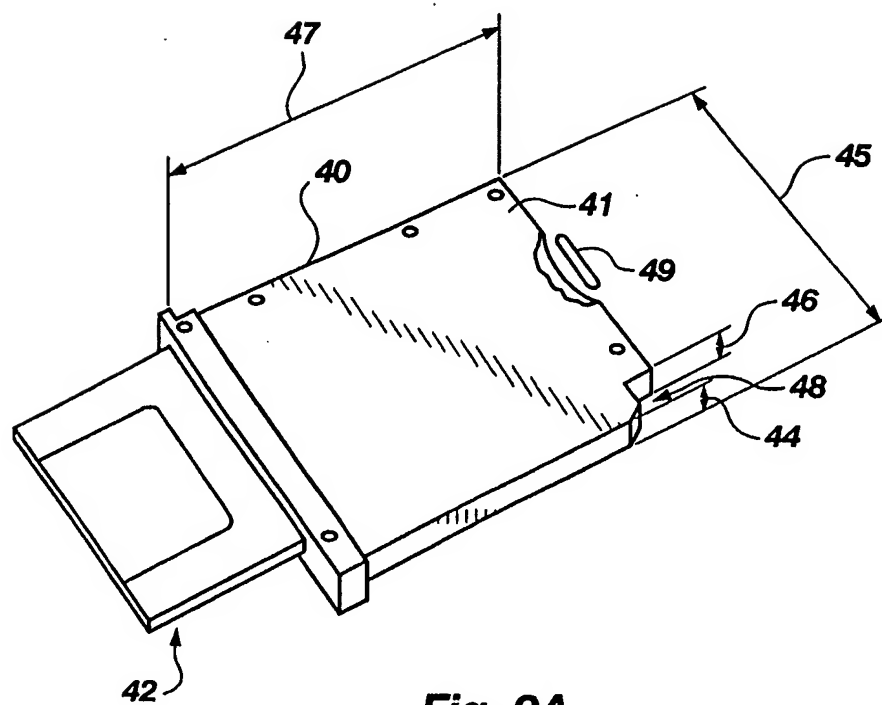
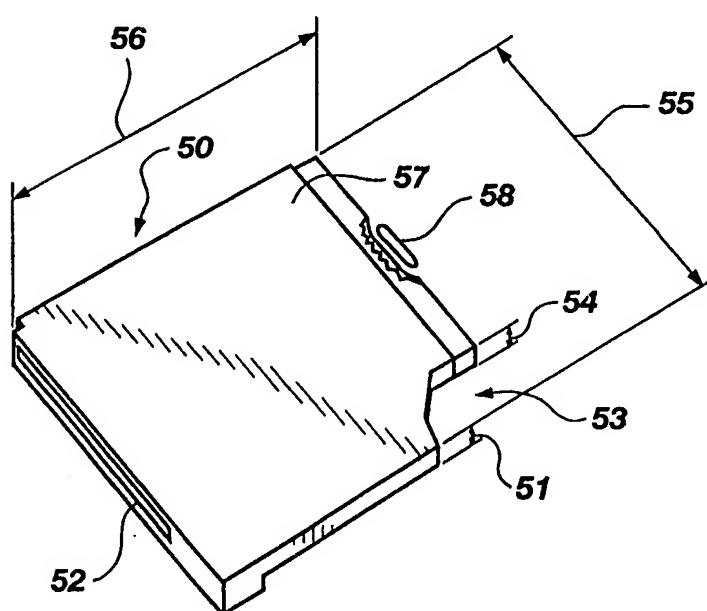
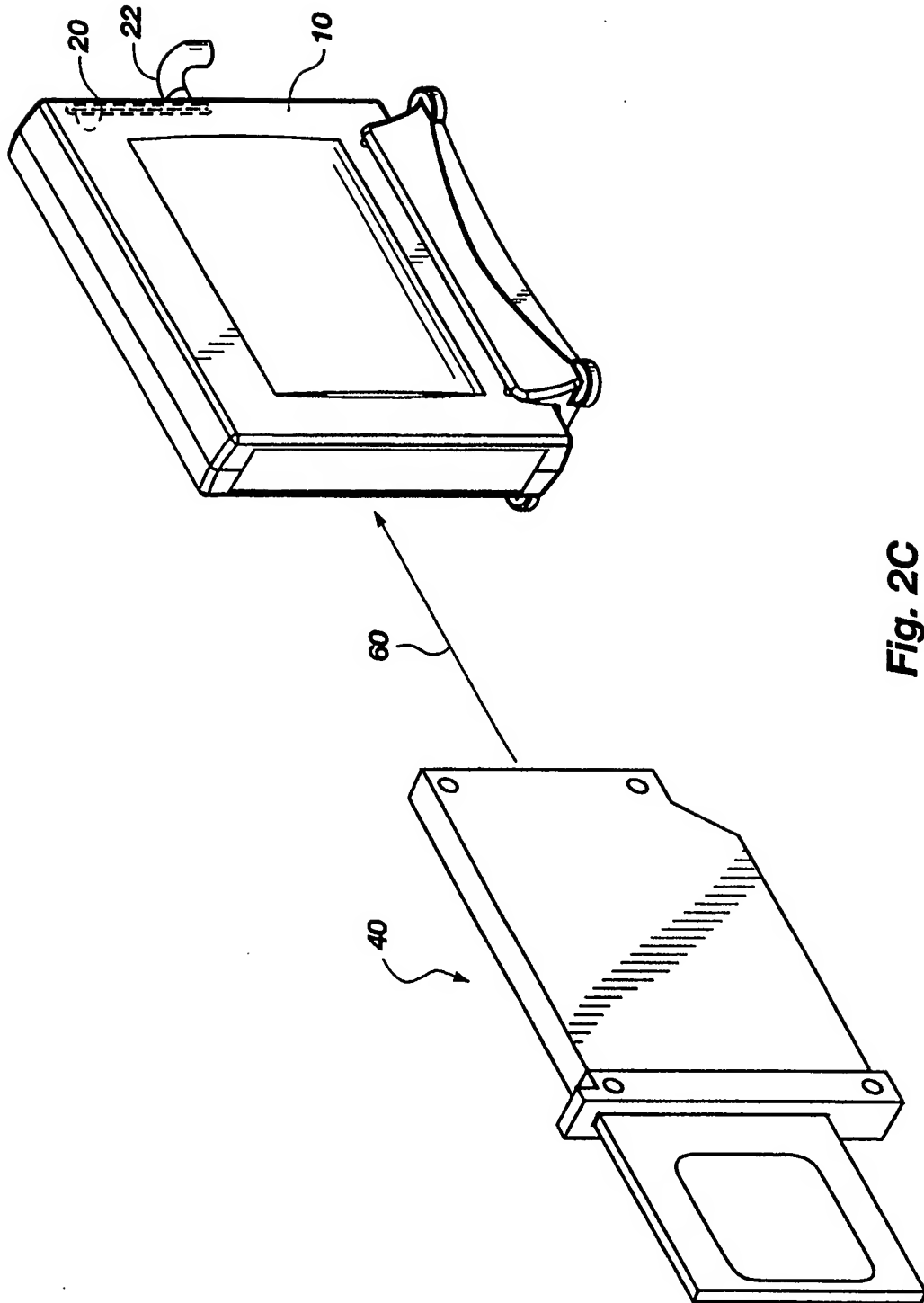


Fig. 2

3/12

**Fig. 2A****Fig. 2B**

4/12



5/12

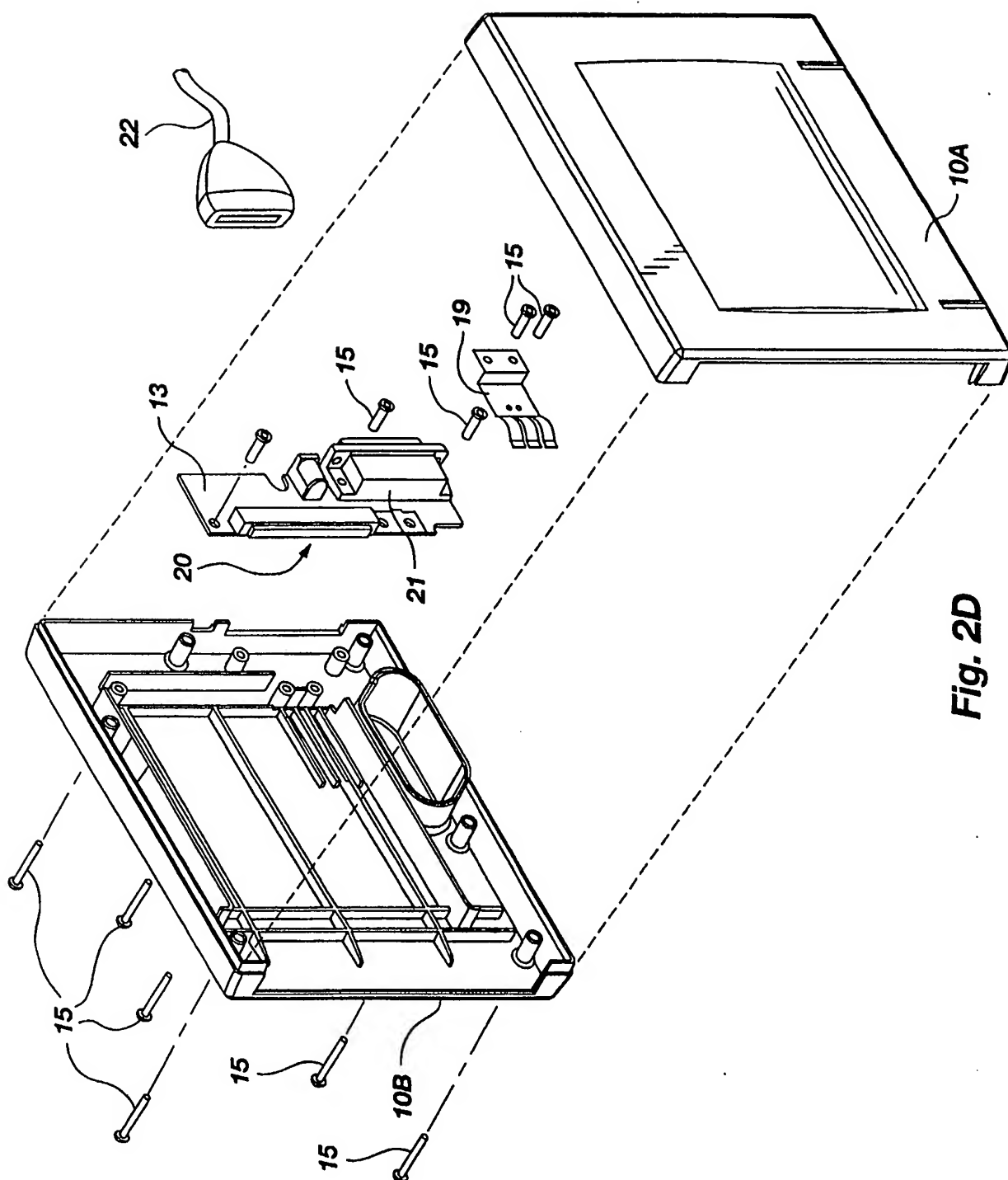
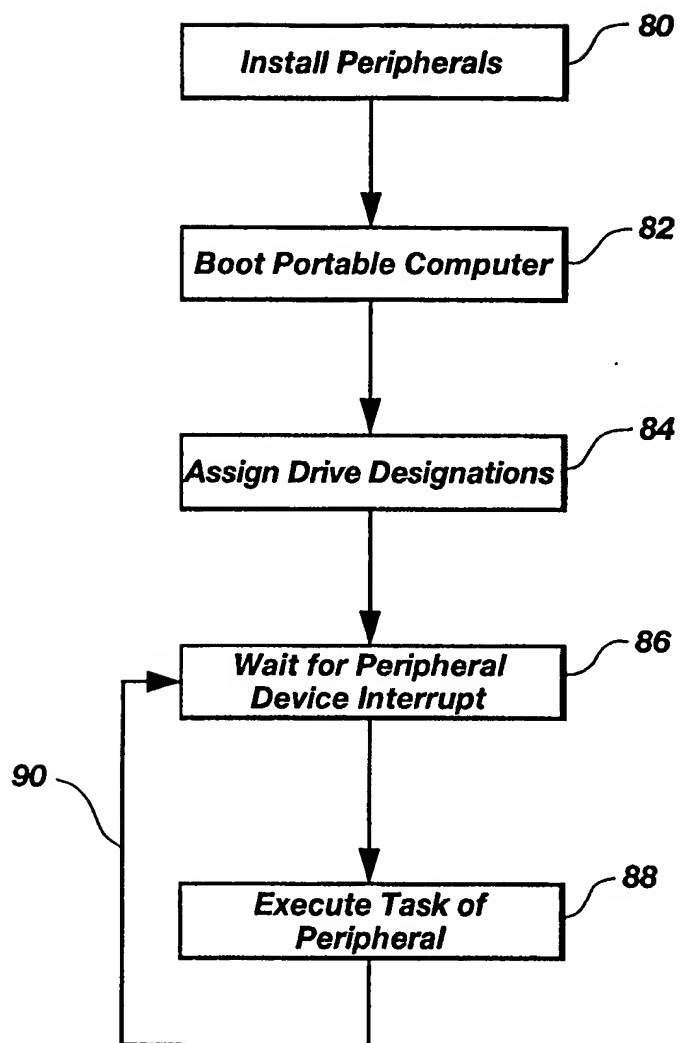
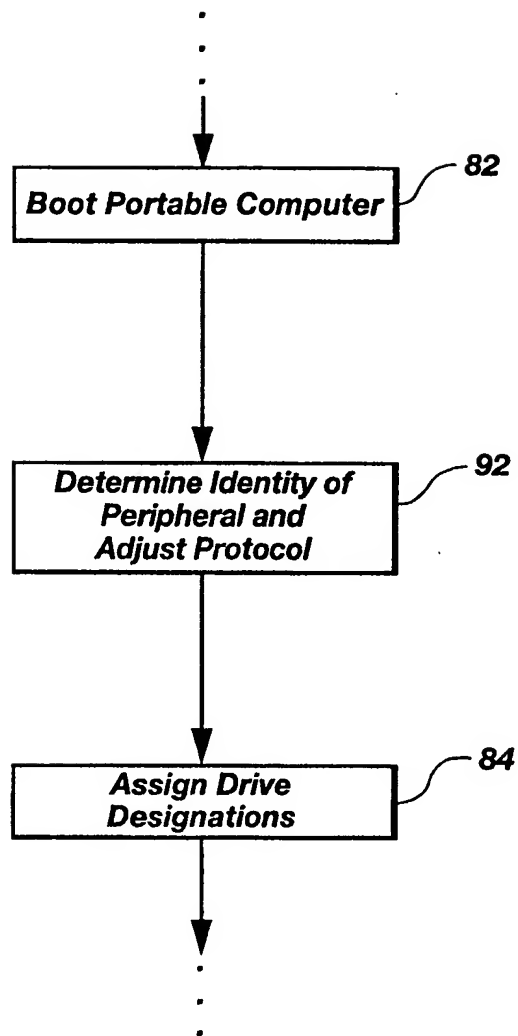


Fig. 2D

6/12

**Fig. 3**

7/12

**Fig. 4**

8/12

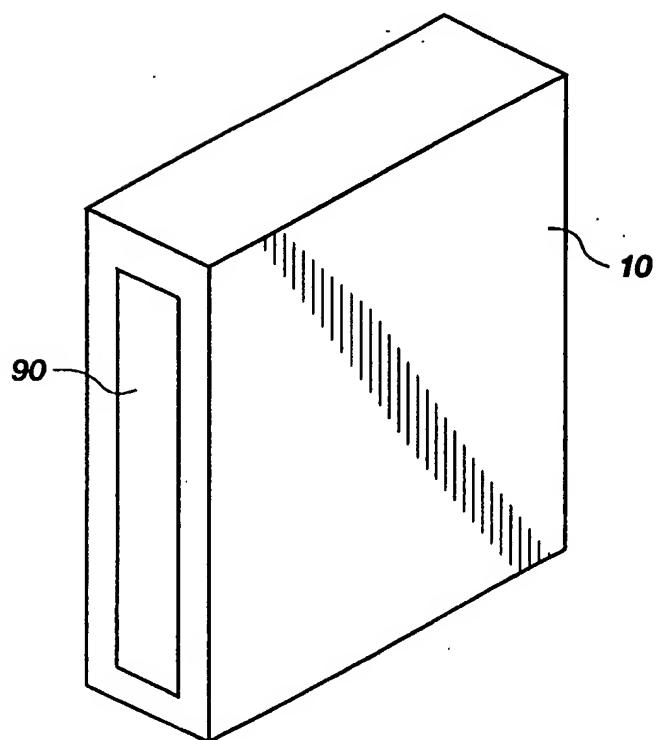


Fig. 5

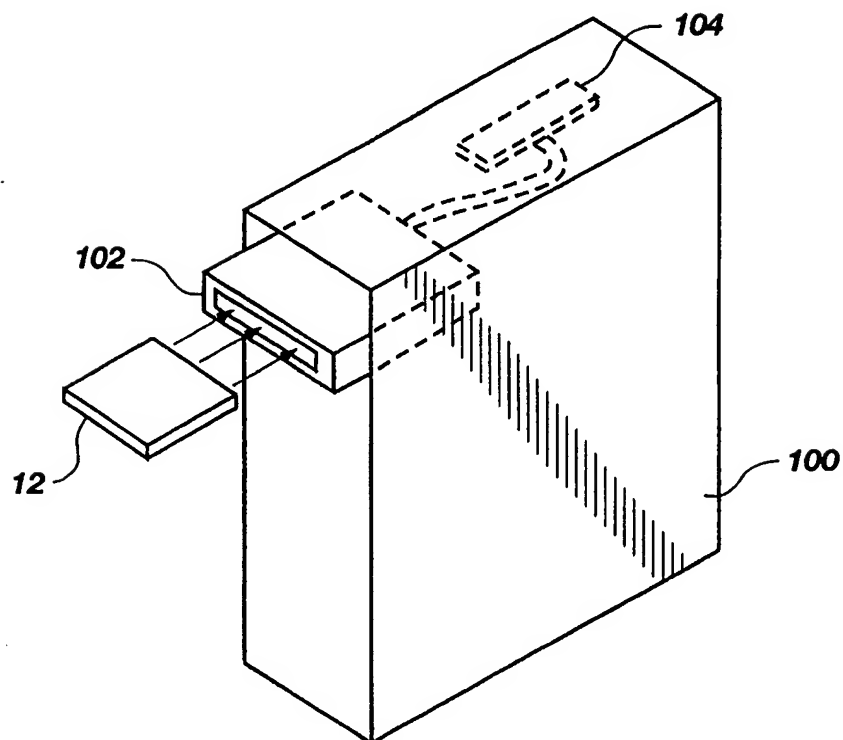
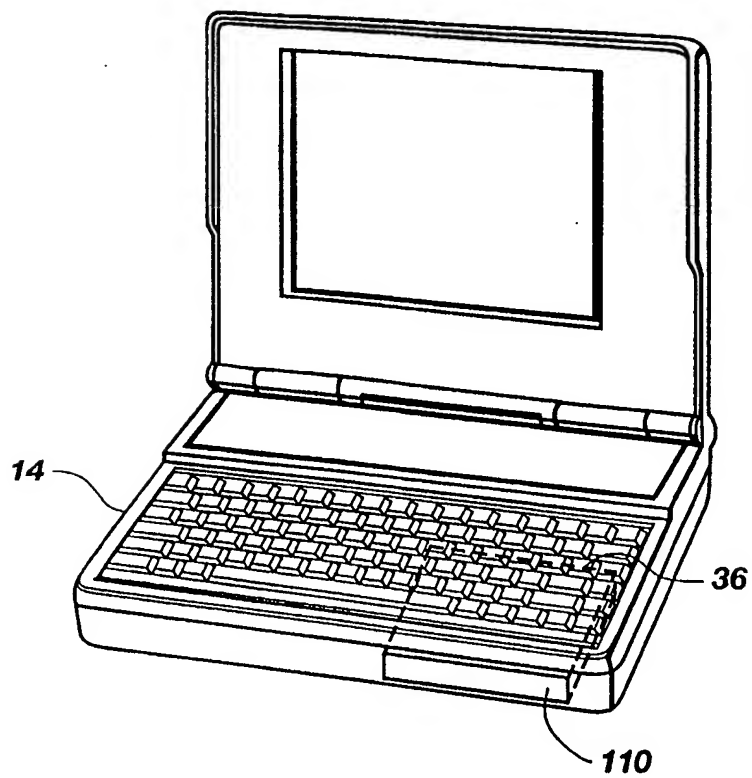
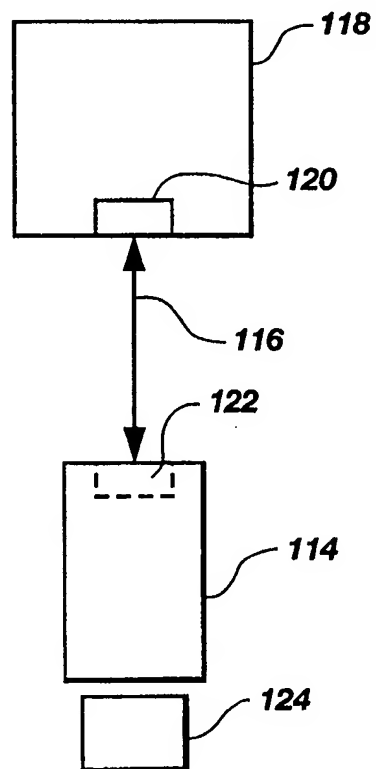


Fig. 6

9/12

**Fig. 7****Fig. 8**

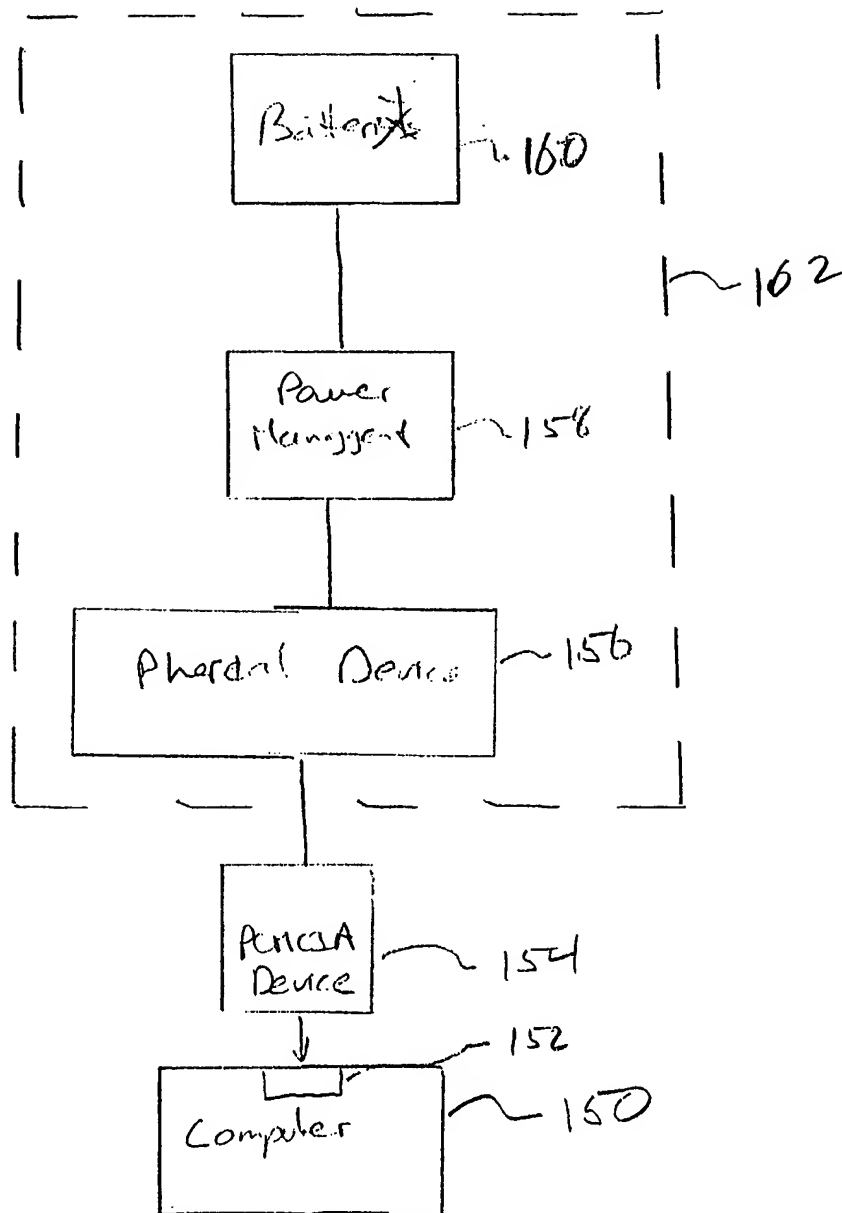


FIG 9

11/12

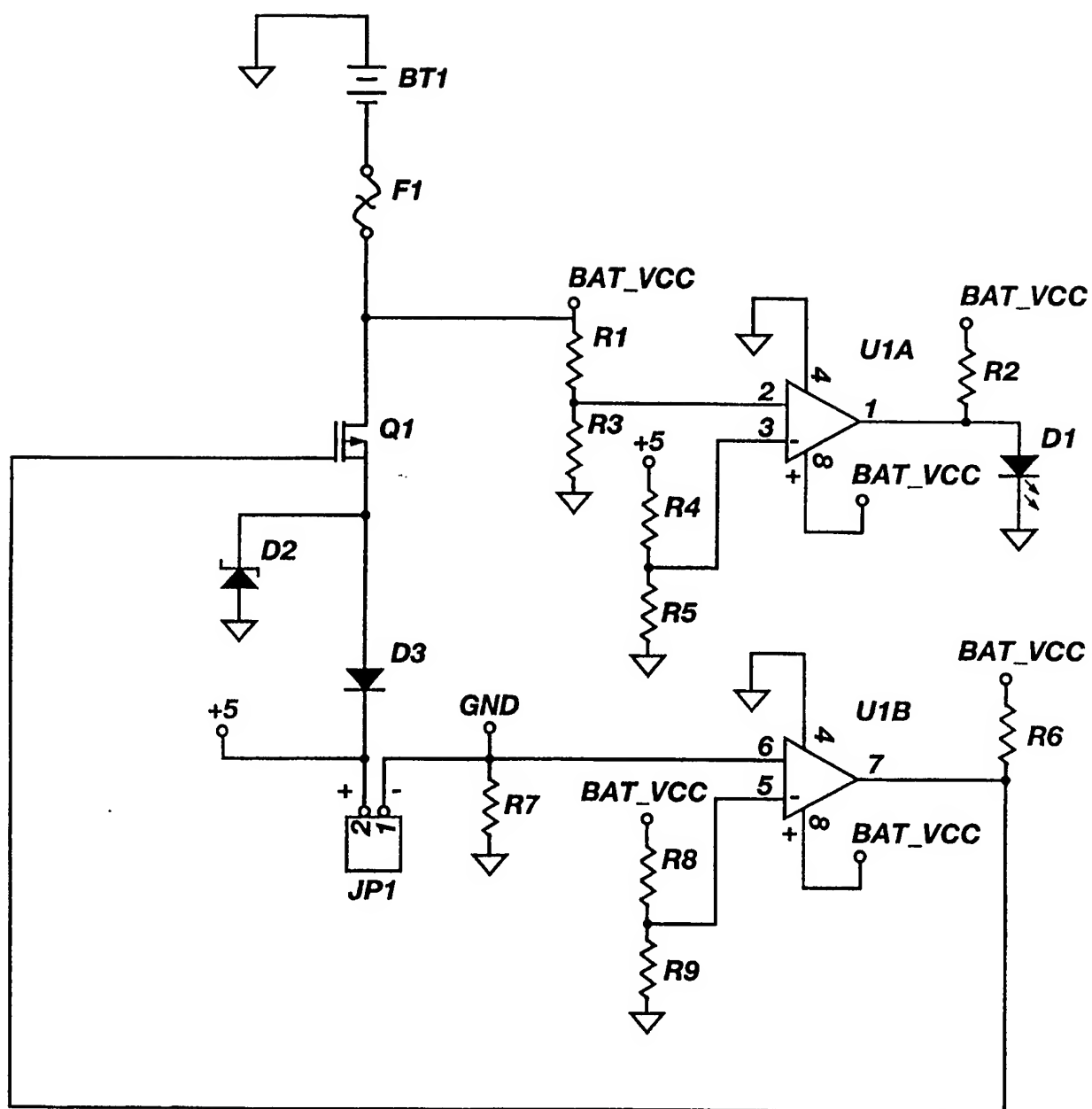


Fig. 10

12/12

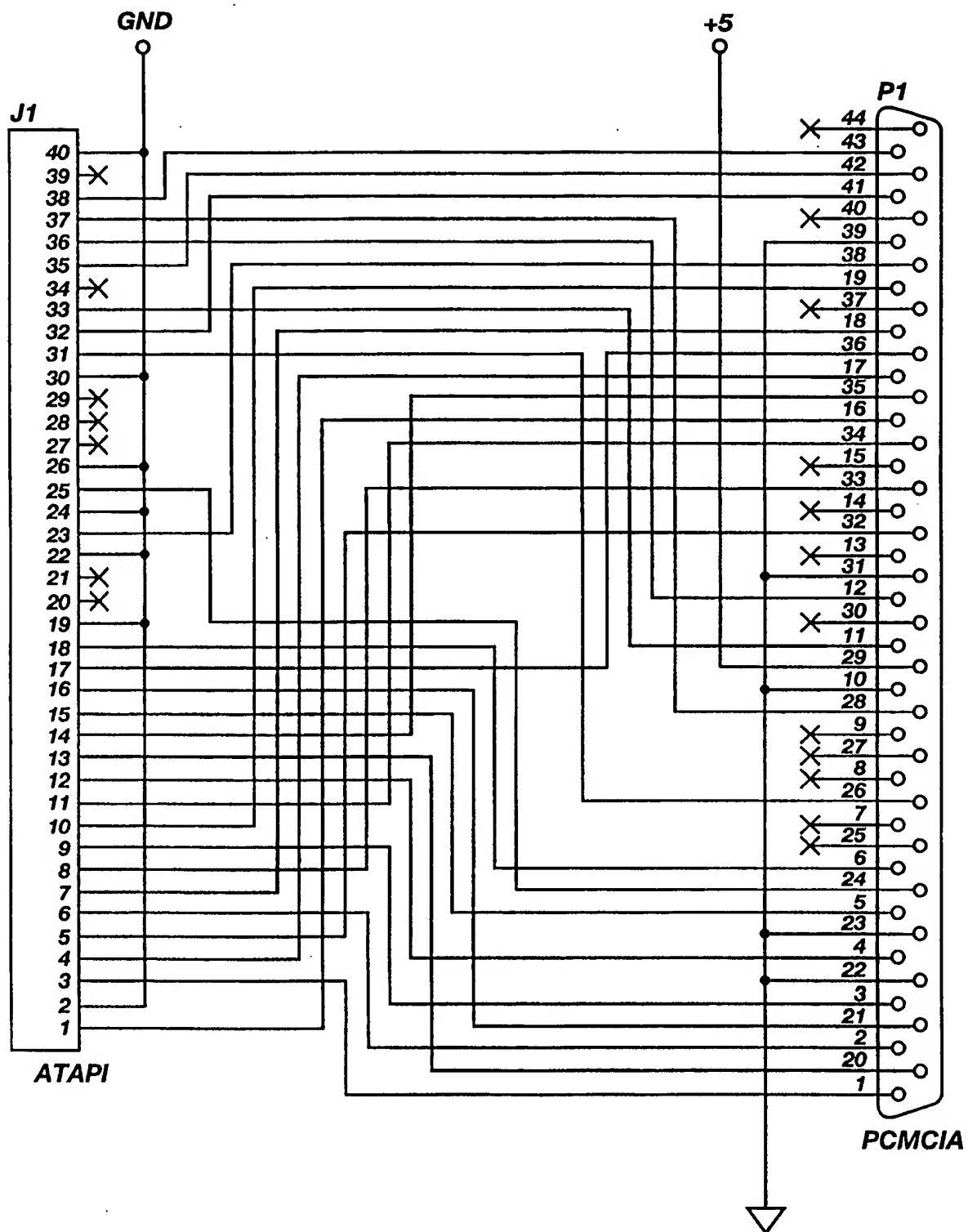


Fig. 11